

SCA74 U.S. PTO

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10/25/02

PATENT NUMBER and  
ISSUE DATE

## U.S. UTILITY Patent Application

APPL NUM	FILING DATE	CLASS	SUBCLASS	GAU	EXAMINER
10065503	10/25/2002	714	726	2133	<i>Dr. C. G. G. G.</i>

**\*\*APPLICANTS:** Corbin William; Kessler Brian; Nelson Erik; Obremski Thomas;  
Wheater Donald;

**\*\*CONTINUING DATA VERIFIED:** NONE  
*ju oklay*

**\*\* FOREIGN APPLICATIONS VERIFIED:** NONE  
*ju oklay*

PG-PUB	DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>
Foreign priority claimed <input type="checkbox"/> yes <input checked="" type="checkbox"/> no		ATTORNEY DOCKET NO
35 USC 119 conditions met <input type="checkbox"/> yes <input checked="" type="checkbox"/> no		BUR920010217US1
Verified and Acknowledged Examiners's initials <i>ju oklay</i>		
TITLE : Testing logic and embedded memory in parallel		

U.S. DEPT. OF COMM./PAT. & TM-PTO-436L (Rev. 12-94)

733

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Total Claims	Print Claim for O.G.
Assistant Examiner		DRAWING	
Amount Due	Date Paid	Sheets Drwg.	Figs. Drwg.
		Print Fig.	
Primary Examiner		Application Examiner	
PREPARED FOR ISSUE			
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